

Form PTO-1449 (modified)	Atty. Docket No. 62519-00003USPT	Serial No. 10/667,035
List of Patents and Publications for Applicant's INFORMATION DISCLOSURE STATEMENT FEB 20 2004 Page 1 of 1	Applicant David K. Johansen	
	Filing Date: September 22, 2003	Group: 1725

U.S. Patent Documents


Exam. Init.	Ref. Des.	Document Number	Date	Name	Class	Sub Class	Filing Date if App.
U6	A01	4,064,462	12/20/1977	Goehler et al.			
	A02	4,131,505	12/26/1978	Davis, Jr.			
	A03	4,208,001	06/17/1980	Martner			
	A04	4,257,730	03/24/1981	Kawakita			
	A05	4,277,710	07/07/1981	Harwood et al.			
	A06	4,838,639	06/13/1989	Morankar et al.			
	A07	4,914,290	04/03/1990	Hilgart et al.			
	A08	5,295,700	03/22/1994	Crews et al.			
↓	A09	5,798,599	08/25/1998	Harwood			
U6	A10	5,880,580	03/09/1999	Johansen			

Foreign Patent Documents

Exam. Init.	Ref. Des.	Document Number	Date	Country	Class	Sub Class	Translation Yes/No

Other Art (Including Author, Title, Date Pertinent Pages, Etc.)

Exam. Init.	Ref. Des.	Citation
U6		"Hex Buffers/Logic-Level Down Converters High-Performance Silicon-Gate CMOS," Motorola Semiconductor Technical Data, Motorola, Inc. 1995, 6 pages.

EXAMINER: 	DATE CONSIDERED: 9/8/05
EXAMINER: INITIAL IF REFERENCE CONSIDERED, WHETHER OR NOT CITATION IS IN CONFORMANCE WITH MPEP 609; DRAW LINE THROUGH CITATION IF NOT IN CONFORMANCE AND NOT CONSIDERED. INCLUDE COPY OF THIS FORM WITH NEXT COMMUNICATION TO APPLICANT.	